

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	3494442
<b>Application Number:</b>	10730619
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<b>Confirmation Number:</b>	4277
<b>Title of Invention:</b>	Method and circuit configuration for transmitting data between a processor and a hardware arithmetic-logic unit
<b>First Named Inventor/Applicant Name:</b>	Burkhard Becker
<b>Customer Number:</b>	27346
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<b>Attorney Docket Number:</b>	L&L-I0225
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<b>Application Type:</b>	Utility under 35 USC 111(a)

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Payment was successfully received in RAM	\$120
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File Listing:									
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part ./zip	Pages (if appl.)				
1		LL-I0225-amf2.pdf	80574 b4b917dbd11b1bd59079a29254bd5c93 a7a7500	yes	8				
<b>Multipart Description/PDF files in .zip description</b>									
<b>Document Description</b>		<b>Start</b>		<b>End</b>					
Amendment After Final		1		1					
Applicant Arguments/Remarks Made in an Amendment		2		8					
<b>Warnings:</b>									
<b>Information:</b>									
2	Fee Worksheet (PTO-06)	fee-info.pdf	8208 277e541970718fb39451d76fb369fb5f 1874c7c	no	2				
<b>Warnings:</b>									
<b>Information:</b>									
<b>Total Files Size (in bytes):</b>				88782					
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